

• General Description

It combines advanced trench MOSFET technology with a low resistance package to provide extremely low $R_{DS(ON)}$.

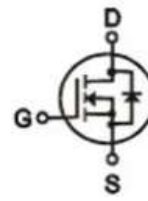
• Features

- Advance high cell density Trench technology
- Low $R_{DS(ON)}$ to minimize conductive loss
- Low Gate Charge for fast switching
- Low Thermal resistance

• Application

- SMPS 2nd Synchronous Rectifier
- BLDC Motor driver

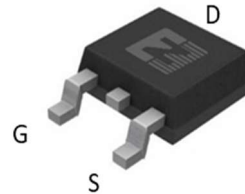
• Product Summary



$V_{DS} = 100V$

$R_{DS(ON)} = 10m\Omega$

$I_D = 46A$



TO-252

• Ordering Information:

Part NO.	ZMS100N10D
Marking	ZMS100N10
Packing Information	REEL TAPE
Basic ordering unit (pcs)	2500

• Absolute Maximum Ratings ($T_C = 25^\circ C$)

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	V_{DS}	100	V
Gate-Source Voltage	V_{GS}	± 20	V
Continuous Drain Current ^②	$I_{D@TC=25^\circ C}$	46	A
	$I_{D@TC=75^\circ C}$	35	A
	$I_{D@TC=100^\circ C}$	29	A
Pulsed Drain Current ^①	I_{DM}	138	A
Total Power Dissipation ^②	$P_{D@TC=25^\circ C}$	60	W
Total Power Dissipation	$P_{D@TA=25^\circ C}$	2.0	W
Operating Junction Temperature	T_J	-55 to 150	$^\circ C$
Storage Temperature	T_{STG}	-55 to 150	$^\circ C$
Single Pulse Avalanche Energy@L=0.1mH	E_{AS}	60	mJ

•Thermal resistance

Parameter	Symbol	Min.	Typ.	Max.	Unit
Thermal resistance, junction - case ^②	R_{thJC}	-	-	2.2	° C/W
Thermal resistance, junction - ambient	R_{thJA}	-	-	64	° C/W
Soldering temperature	T_{sold}	-	-	260	° C

•Electronic Characteristics

Parameter	Symbol	Condition	Min.	Typ	Max.	Unit
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{GS} = 0V, I_D = 250\mu A$	100			V
Gate Threshold Voltage	$V_{GS(TH)}$	$V_{GS} = V_{DS}, I_D = 250\mu A$	1.3		2.5	V
Drain-Source Leakage Current	I_{DSS}	$V_{DS} = 100V, V_{GS} = 0V$			1.0	μA
Gate- Source Leakage Current	I_{GSS}	$V_{GS} = \pm 20V, V_{DS} = 0V$			± 100	nA
Static Drain-source On Resistance	$R_{DS(ON)}$	$V_{GS} = 10V, I_D = 20A$		10	12.8	m Ω
	$R_{DS(ON)}$	$V_{GS} = 4.5V, I_D = 16A$		12.5	15	m Ω
Forward Transconductance	g_{FS}	$V_{DS} = 25V, I_D = 15A$		15		S
Diode Forward Voltage	V_{FSD}	$I_S = 20A$			1.28	V

•Electronic Characteristics

Parameter	Symbol	Condition	Min.	Typ	Max.	Unit
Input capacitance	C_{iss}	$V_{GS} = 0V, V_{DS} = 25V$ $f = 1MHz$	-	1400	-	pF
Output capacitance	C_{oss}		-	630	-	
Reverse transfer capacitance	C_{rss}		-	33	-	

Gate Charge characteristics($T_a = 25^\circ C$)

Parameter	Symbol	Condition	Min.	Typ	Max.	Unit
Total gate charge	Q_g	$V_{DD} = 30V$	-	20	-	nC
Gate - Source charge	Q_{gs}	$I_D = 8A$	-	3.6	-	
Gate - Drain charge	Q_{gd}	$V_{GS} = 10V$	-	2.8	-	

Note: ① Pulse Test : Pulse width $\leq 300\mu s$, Duty cycle $\leq 2\%$;

② Device mounted on FR-4 substrate PC board, 2oz copper, with thermal bias to bottom layer 1inch square copper plate;

Fig.1 Gate-Charge Characteristics

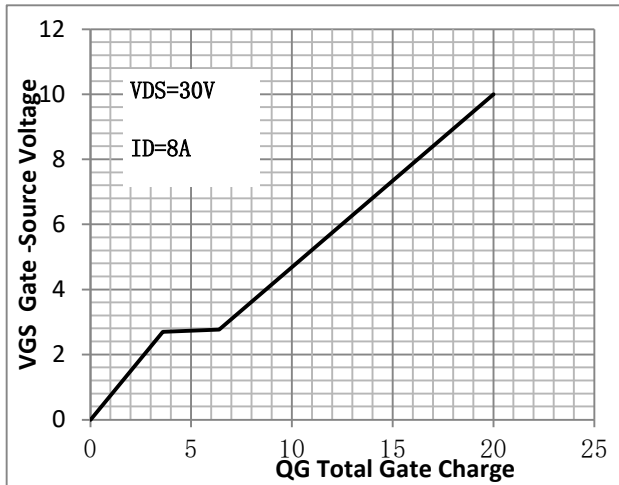


Fig.2 Capacitance Characteristics

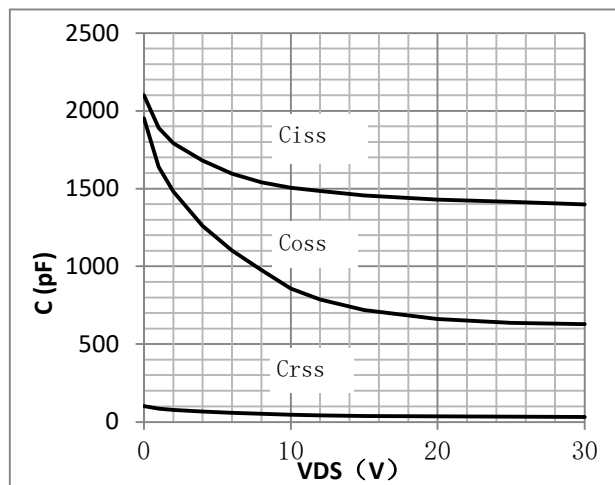


Fig.3 Power Dissipation

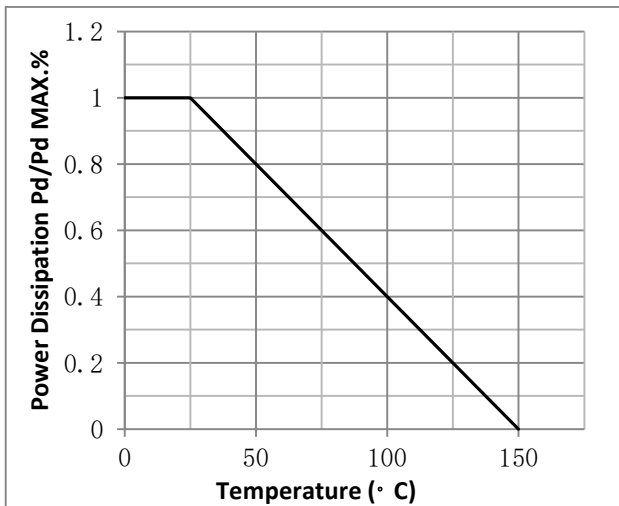


Fig.4 Typical output Characteristics

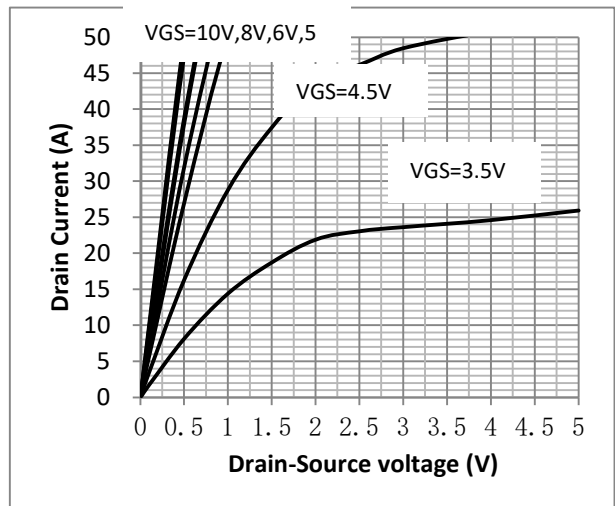


Fig.5 Threshold Voltage V.S Junction Temperature

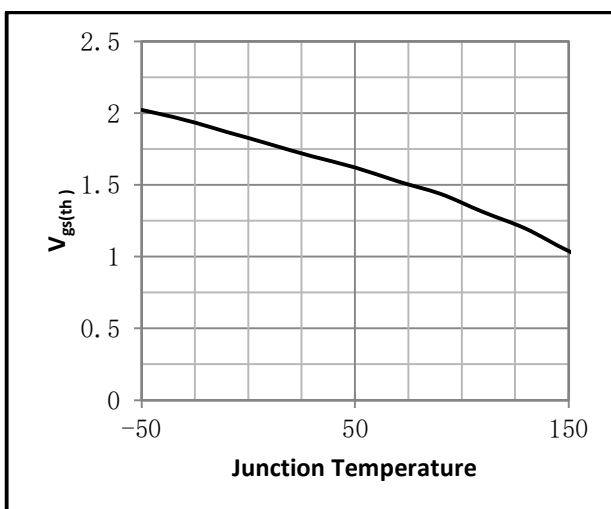


Fig.6 Resistance V.S Drain Current

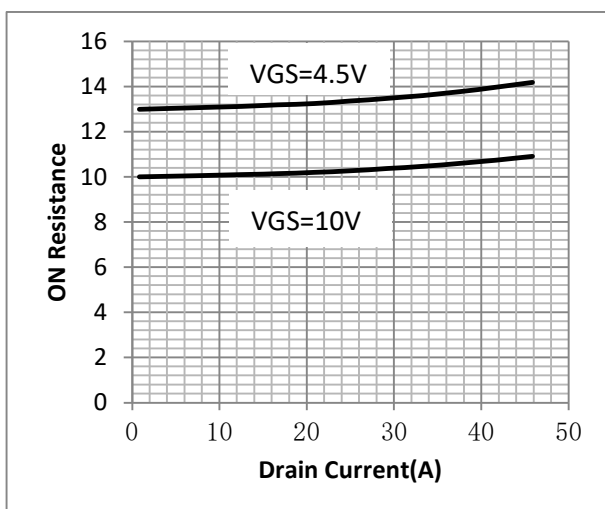


Fig.7 On-Resistance VS Gate Source Voltage

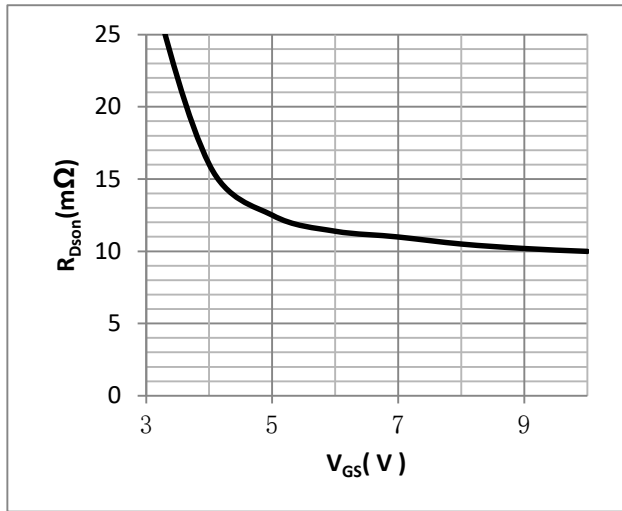


Fig.8 On-Resistance V.S Junction Temperature

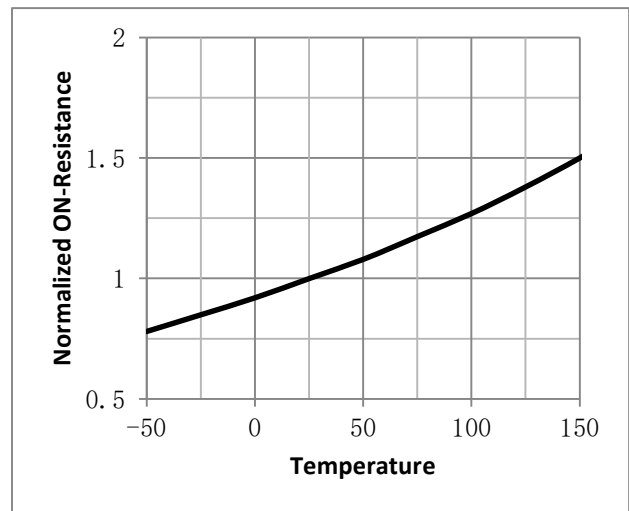


Fig.9 SOA Maximum Safe Operating Area

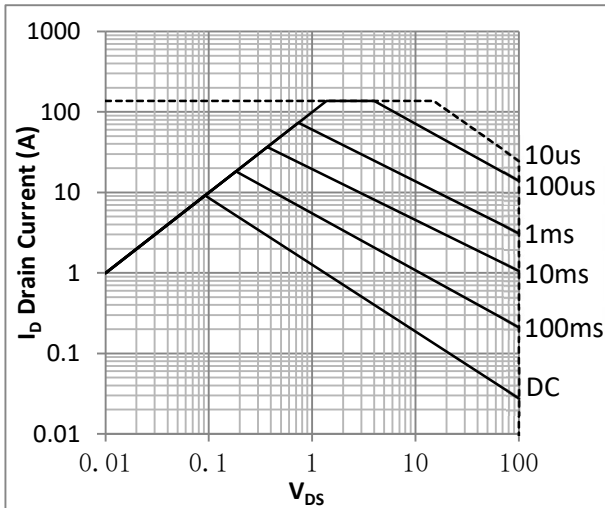


Fig.10 I_D -Junction Temperature

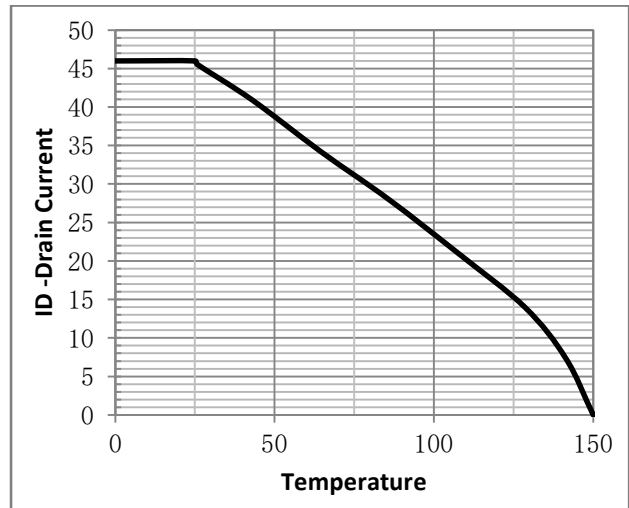


Figure 11. Diode Forward Voltage vs. Current

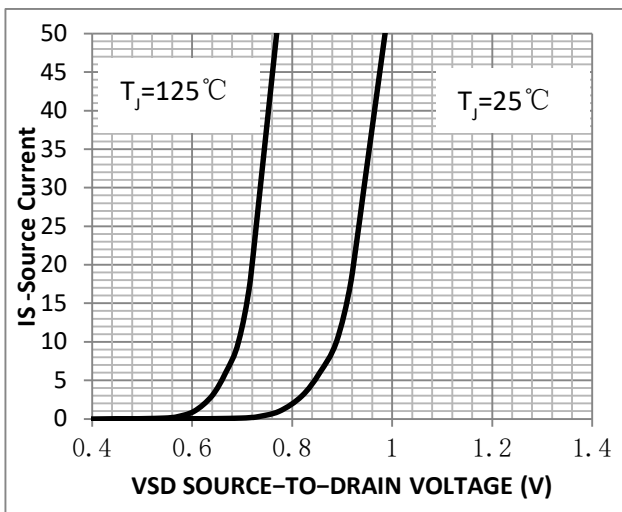


Figure 12. Transfer Characteristics

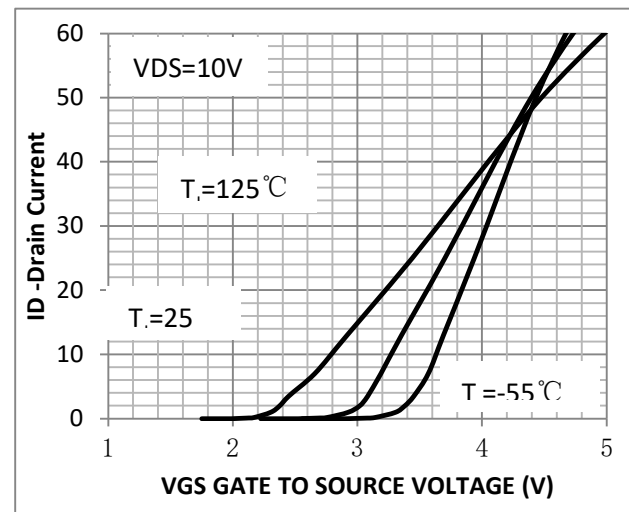


Fig.12 Gate Charge Measurement Circuit

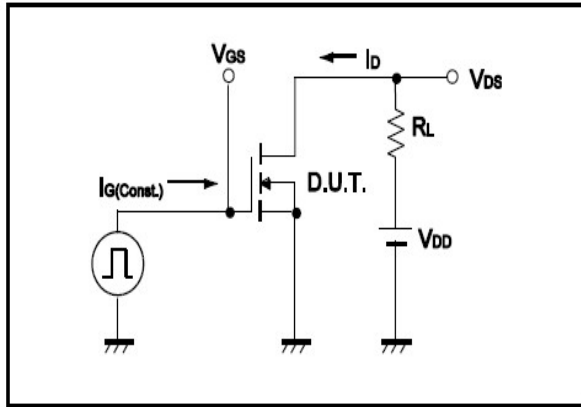


Fig.13 Gate Charge Waveform

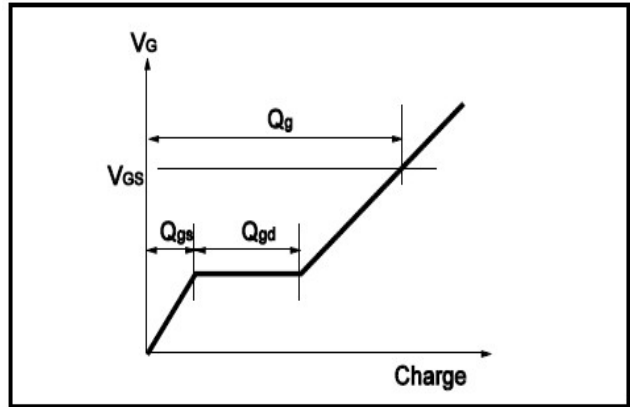


Fig.14 Switching Time Measurement Circuit

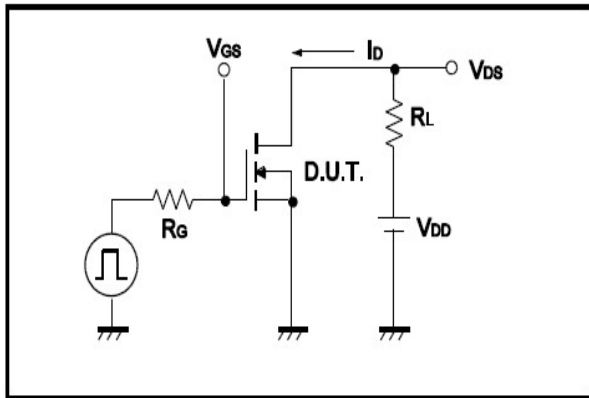


Fig.15 Switching Time Waveform

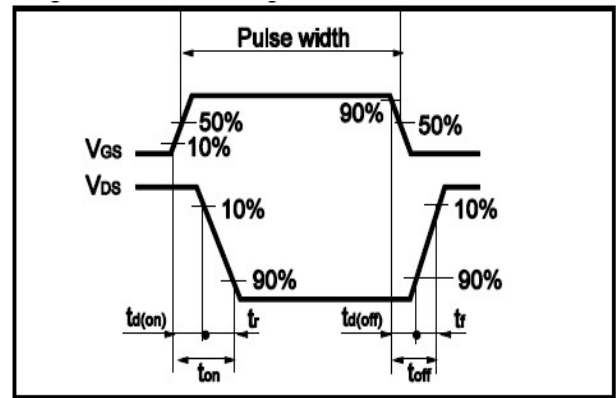


Fig.16 Avalanche Measurement Circuit

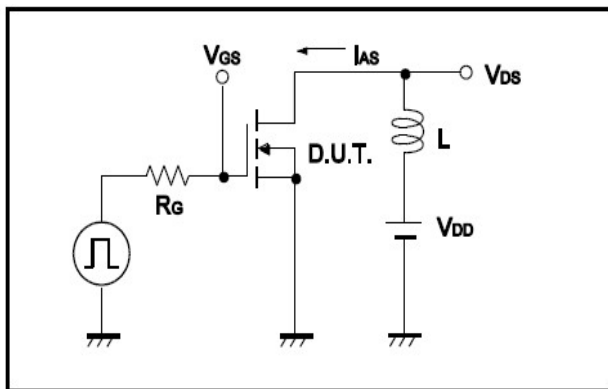
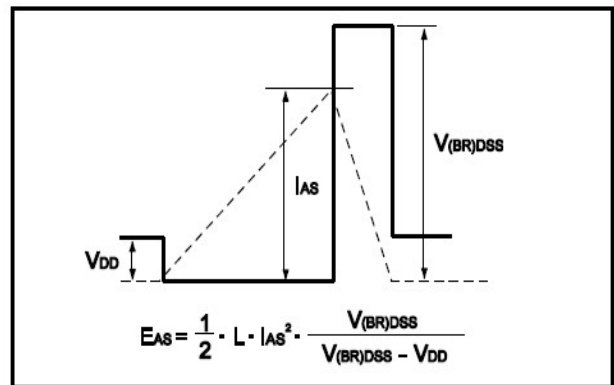
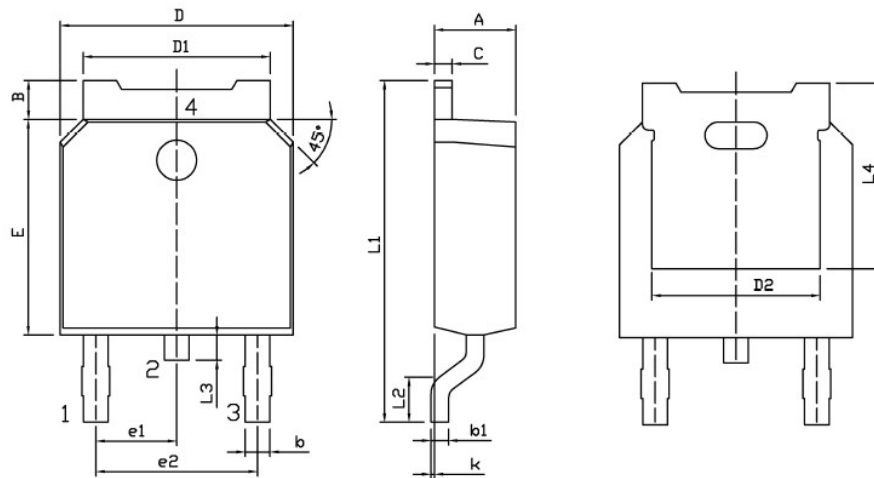


Fig.17 Avalanche Waveform





• Dimensions (TO-252)



Land Pattern
(Only for Reference)

Dimensions In Millimeters					
Symbol	MIN	MAX	Symbol	MIN	MAX
A	2.20	2.40	E	5.95	6.25
B	0.95	1.25	e1	2.24	2.34
b	0.70	0.90	e2	4.43	4.73
b1	0.45	0.55	L1	9.85	10.35
C	0.45	0.55	L2	1.70	2.00
D	6.45	6.75	L3	0.60	0.90
D1	5.10	5.50	L4	5.05	
D2	4.85		k	0.00	0.10

